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L2: (0) (more adj3 than) same l1
L3: (69) predetermin$3 same l1
L4: (2950) signal adj3 (wait$3 or assert$3) adj4 (period or time)
L5: (461) 710/$.cccls. and 14
L6: (0) mask$3 same 14
L7: (116) timer same 14
L8: (26) 710/$.cccls. and 17
L9: (10481) wait adj2 state
L10: (4) 710/300-304.cccls. and 17
L11: (66) 710/300-304.cccls. and 19
L12: (1068) request$3 same signal same 19
L13: (367) 710/$.cccls. and 112
L14: (19) 710/300-304.cccls. and 112
L15: (4) ((keep$3 or kept) adj assert$3) same (predetermin$3 adj3 (time or period))
L16: (55) ((keep$3 or kept) adj assert$3) same ((time or period))
L21: (2571) wait adj signal
L22: (33) read same write same (expand$3 or extend$3) same 121
L33: (15) 122 and 'or'
L40: (1) "6049844".PN.
L41: (1) "5774684".PN.
L42: (1) "5649128".PN.
L43: (93) (710/312).CCCLS.
L44: (1) 710/312.cccls. and 121
L45: (5) (read same write same (expand$3 or extend$3)) and 710/312.cccls.
L46: (6261) hold same arbit$6
L47: (714) 710/$.cccls. and 146
L48: (54) hold same arbit$6 same (bridge or interface) same signal
L49: (30) 710/$.cccls. and 148
L50: (26) 146 and phan.xa.
L51: (1) "5737545".PN.
L52: (1) "5717876".PN.
L53: (1) "5717873".PN.
L54: (1) "5708794".PN.
L55: (1) "5625779".PN.
L56: (1) "5596729".PN.
L57: (1) "5596729".PN.
L58: (1) "5559968".PN.
L59: (1) "5533204".PN.
L60: (206) 'or' same l1
L61: (76) 'or' same 121
L62: (1) 'or' same bridge same 121
L63: (14) 'or' and bridge same 121
L64: (1) wait$3 same (logic$3 adj2 'or') same bridge
L65: (25) wait$3 same ('or') same bridge
L66: (119) read same write same (expand$3 or extend$3) same bridge
L67: (50) 'or' and 166
L68: (8) read same write same ((expand$3 or extend$3) near3 cycle) same bridge
L79: (1459) 'or' same bridge
L80: (5) 710/305.cccls. and 179
L81: (56) 710/305-317.cccls. and 179
L88: (14) master adj3 connect$3 adj4 'or'
L89: (41) bridge same interface same 'or'

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	U	P	PT	P	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C
1	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6594720 B1	20030715	37	Data processing system having a PC card type interface with	710/301	710/8; 711/202		Matsui, Shigezumi et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6477596 B2	20021105	9	Bus controlling method and	710/107	710/113;		Miura, Hiroki et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>